

## EAST Search History

| Ref # | Hits | Search Query  | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|------|---|---|------------------|---------|------------------|
| L1    | 11   | "626192"  | US-PGPUB; USPAT                                 | OR               | ON      | 2007/03/28 17:19 |
| S1    | 328  | priority NEAR50 preemption AND (task\$1 OR thread\$1) AND @ay<"2003"  | US-PGPUB; USPAT                                 | ADJ              | ON      | 2007/03/26 11:20 |
| S2    | 74   | S1 AND "718".CLAS.  | US-PGPUB; USPAT                                 | ADJ              | ON      | 2007/03/26 10:55 |
| S3    | 0    | prioirit\$3 NEAR5 (boost OR increas\$3 OR chang\$3 OR decreas\$3) NEAR5 delay\$4 NEAR20 preemption AND @ay<"2003" | US-PGPUB; USPAT                                 | ADJ              | ON      | 2007/03/26 10:59 |
| S4    | 0    | prioirit\$3 NEAR5 delay\$4 NEAR20 preemption AND @ay<"2003"   | US-PGPUB; USPAT                                 | ADJ              | ON      | 2007/03/26 10:59 |
| S5    | 3    | priorit\$3 NEAR5 (boost OR increas\$3 OR chang\$3 OR décreas\$3) NEAR5 delay\$4 NEAR20 preemption AND @ay<"2003"  | US-PGPUB; USPAT                                 | ADJ              | ON      | 2007/03/27 12:52 |
| S6    | 0    | priorit\$3 NEAR5 boost NEAR5 delay\$4 NEAR20 preemption AND @ay<"2003"  | US-PGPUB; USPAT                                 | ADJ              | ON      | 2007/03/26 11:05 |
| S7    | 6    | priorit\$3 NEAR5 delay\$4 NEAR20 preemption AND @ay<"2003"  | US-PGPUB; USPAT                                 | ADJ              | ON      | 2007/03/26 11:05 |
| S8    | 2    | ("20020120661"   "6110220").PN.   | US-PGPUB; USPAT; USOCR                          | OR               | ON      | 2007/03/26 11:16 |
| S9    | 0    | priority NEAR20 (detect\$4 ADJ3 preemption) AND @ay<"2003"  | US-PGPUB; USPAT; USOCR                          | ADJ              | ON      | 2007/03/26 11:18 |
| S10   | 0    | priority NEAR20 (detect\$4 ADJ3 preemption) AND @ay<"2003"  | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT | ADJ              | ON      | 2007/03/26 11:19 |
| S11   | 4    | priority NEAR50 (detect\$4 ADJ10 preemption) AND @ay<"2003"   | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT | ADJ              | ON      | 2007/03/26 11:20 |
| S12   | 530  | priority NEAR50 (preemption) AND @ay<"2003"   | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT | ADJ              | ON      | 2007/03/26 15:30 |

## EAST Search History

|     |     |   |                        |     |    |                  |
|-----|-----|---|------------------------|-----|----|------------------|
| S13 | 81  | (task\$1 OR thread\$1) NEAR10 priority NEAR50 preemption AND @ay<"2003"   | US-PGPUB; USPAT        | ADJ | ON | 2007/03/26 11:32 |
| S14 | 12  | S13 AND (critical section)  | US-PGPUB; USPAT        | ADJ | ON | 2007/03/26 11:25 |
| S15 | 11  | ("5274809"   "5515538"   "5666523"   "5701432"   "5720018"   "5724503"   "5754173"   "5757914"   "5765151"   "5765157"   "5767851").PN. | US-PGPUB; USPAT; USOCR | OR  | ON | 2007/03/26 11:26 |
| S16 | 49  | (task\$1 OR thread\$1) NEAR10 (priority NEAR10 (increas\$3 OR higher OR shift\$3 OR chang\$3)) NEAR50 preemption AND @ay<"2003"         | US-PGPUB; USPAT        | ADJ | ON | 2007/03/26 11:51 |
| S17 | 10  | (dynamic\$5 NEAR10 priority) NEAR50 preemption AND @ay<"2003"   | US-PGPUB; USPAT        | ADJ | ON | 2007/03/26 11:54 |
| S18 | 0   | (dynamic priority) NEAR50 preemption AND @ay<"2003"   | US-PGPUB; USPAT        | ADJ | ON | 2007/03/26 11:54 |
| S19 | 131 | ((increas\$3 OR rais\$3 OR boost\$3) NEAR5 priorit\$3) AND preemption AND @ay<"2003"  | US-PGPUB; USPAT        | ADJ | ON | 2007/03/26 12:07 |
| S20 | 6   | ((increas\$3 OR rais\$3 OR boost\$3) NEAR5 priorit\$3) NEAR50 preemption AND @ay<"2003"   | US-PGPUB; USPAT        | ADJ | ON | 2007/03/26 12:07 |
| S21 | 22  | "6038516"   | US-PGPUB; USPAT        | OR  | ON | 2007/03/26 13:17 |
| S22 | 8   | "422037"  | US-PGPUB; USPAT        | OR  | ON | 2007/03/26 13:19 |
| S23 | 4   | "6845504"   | US-PGPUB; USPAT        | OR  | ON | 2007/03/26 13:20 |
| S24 | 21  | "6477562"   | US-PGPUB; USPAT        | OR  | ON | 2007/03/26 13:21 |
| S25 | 19  | "6430593"   | US-PGPUB; USPAT        | OR  | ON | 2007/03/26 13:21 |
| S26 | 12  | "6167424"   | US-PGPUB; USPAT        | OR  | ON | 2007/03/26 13:28 |
| S27 | 19  | "301104"  | US-PGPUB; USPAT        | OR  | ON | 2007/03/26 13:28 |
| S28 | 0   | (raising OR increasing) NEAR5 priorit\$3 NEAR10 prevent\$3 NEAR10 preemption AND @ay<"2003"   | US-PGPUB; USPAT        | OR  | ON | 2007/03/26 15:16 |
| S29 | 0   | (raising OR increasing) NEAR5 priorit\$3 NEAR10 preemption AND @ay<"2003"   | US-PGPUB; USPAT        | OR  | ON | 2007/03/26 15:16 |

## EAST Search History

|     |     |  |   |     |    |                  |
|-----|-----|--|---|-----|----|------------------|
| S30 | 3   | (raising OR increasing) NEAR5 priorit\$3 NEAR30 preemption AND @ay<"2003"                                | US-PGPUB; USPAT                                 | ADJ | ON | 2007/03/26 15:17 |
| S31 | 4   | (raising OR increasing OR changing) NEAR5 priorit\$3 NEAR30 preemption AND @ay<"2003"                    | US-PGPUB; USPAT                                 | ADJ | ON | 2007/03/26 15:18 |
| S32 | 13  | (rais\$3 OR increas\$3 OR chang\$3) NEAR5 priorit\$3 NEAR100 preemption AND @ay<"2003"                   | US-PGPUB; USPAT                                 | ADJ | ON | 2007/03/26 15:18 |
| S33 | 530 | priority NEAR50 (preemption) AND @ay<"2003"  | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT | ADJ | ON | 2007/03/26 15:31 |
| S34 | 81  | S33 and "718".CLAS.  | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT | ADJ | ON | 2007/03/26 16:31 |
| S35 | 26  | ((rais\$3 OR chang\$3 OR increas\$3) NEAR3 priorit\$3) SAME (critical section) AND @ay<"2003"            | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT | ADJ | ON | 2007/03/26 16:39 |
| S36 | 47  | ((rais\$3 OR chang\$3 OR increas\$3) NEAR3 priorit\$3) SAME ((task OR context) switch\$3) AND @ay<"2003" | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT | ADJ | ON | 2007/03/26 18:02 |
| S37 | 47  | "4736318"  | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT | ADJ | ON | 2007/03/26 18:03 |
| S38 | 2   | "4736318".pn.  | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT | ADJ | ON | 2007/03/26 18:03 |
| S39 | 11  | "626192"   | US-PGPUB; USPAT                                 | ADJ | ON | 2007/03/27 12:53 |

Day : Wednesday

Date: 3/28/2007

Time: 19:29:51

**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = ACCAPADI

First Name = JOS

| Application#                    | Patent#    | Status | Date Filed | Title   | Inventor Name        |
|---------------------------------|------------|--------|------------|---|----------------------|
| <a href="#"><u>11201642</u></a> | Not Issued | 30     | 08/11/2005 | Method and apparatus for aging data in a cache  | ACCAPADI, JOS        |
| <a href="#"><u>11146453</u></a> | Not Issued | 30     | 06/06/2005 | Administration of locks for critical sections of computer programs in a computer that supports a multiplicity of logical partitions | ACCAPADI, JOS M.     |
| <a href="#"><u>11227031</u></a> | Not Issued | 30     | 09/15/2005 | Facilitating presentation and monitoring of electronic mail messages with reply by constraints                                      | ACCAPADI, JOS M.     |
| <a href="#"><u>11259898</u></a> | Not Issued | 30     | 10/27/2005 | System and method for implementing a fast file synchronization in a data processing system  | ACCAPADI, JOS M.     |
| <a href="#"><u>10286532</u></a> | Not Issued | 168    | 10/31/2002 | System and method for preferred memory affinity   | ACCAPADI, JOS MANUEL |
| <a href="#"><u>10422037</u></a> | Not Issued | 71     | 04/23/2003 | System and method for thread prioritization during lock processing  | ACCAPADI, JOS MANUEL |
| <a href="#"><u>10626192</u></a> | Not Issued | 30     | 07/24/2003 | System and method for delayed priority boost  | ACCAPADI, JOS MANUEL |
| <a href="#"><u>10671057</u></a> | Not Issued | 30     | 09/25/2003 | System and method for CPI load balancing in SMT processors  | ACCAPADI, JOS MANUEL |
| <a href="#"><u>10671132</u></a> | Not Issued | 30     | 09/25/2003 | System and method for CPI scheduling on SMT processors  | ACCAPADI, JOS MANUEL |
| <a href="#"><u>10782668</u></a> | Not Issued | 30     | 02/19/2004 | User defined preferred DNS reference  | ACCAPADI, JOS MANUEL |
| <a href="#"><u>10803659</u></a> | Not Issued | 25     | 03/18/2004 | Method and data processing system for per-chip thread queuing in a multi-processor system   | ACCAPADI, JOS MANUEL |
| <a href="#"><u>10828455</u></a> | 7120753    | 150    | 04/20/2004 | SYSTEM AND METHOD FOR   | ACCAPADI, JOS        |

|                 |            |    |            |  |                      |
|-----------------|------------|----|------------|--|----------------------|
|                 |            |    |            | DYNAMICALLY ADJUSTING READ AHEAD VALUES BASED UPON MEMORY USAGE  | MANUEL               |
| <u>10834498</u> | Not Issued | 30 | 04/29/2004 | Scheduling threads in a multi-processor computer   | ACCAPADI, JOS MANUEL |
| <u>10855732</u> | Not Issued | 30 | 05/27/2004 | Method, computer program product, and data processing system for improving transaction-oriented client-server application performance          | ACCAPADI, JOS MANUEL |
| <u>10860409</u> | Not Issued | 71 | 06/03/2004 | Nodelay per port   | ACCAPADI, JOS MANUEL |
| <u>10916976</u> | Not Issued | 25 | 08/12/2004 | Scheduling threads in a multiprocessor computer  | ACCAPADI, JOS MANUEL |
| <u>10916984</u> | Not Issued | 30 | 08/12/2004 | System, application and method of reducing cache thrashing in a multi-processor with a shared cache on which a disruptive process is executing | ACCAPADI, JOS MANUEL |
| <u>10916985</u> | Not Issued | 25 | 08/12/2004 | System, apparatus and method of reducing adverse performance impact due to migration of processes from one CPU to another                      | ACCAPADI, JOS MANUEL |
| <u>10996307</u> | Not Issued | 61 | 11/22/2004 | Interrupt thresholding for SMT and multi processor systems   | ACCAPADI, JOS MANUEL |
| <u>11301104</u> | Not Issued | 30 | 12/12/2005 | System and method for optimized preemption and reservation of software locks   | ACCAPADI, JOS MANUEL |
| <u>11334676</u> | Not Issued | 30 | 01/18/2006 | Email application smart paste entry feature  | ACCAPADI, JOS MANUEL |
| <u>11424651</u> | Not Issued | 30 | 06/16/2006 | METHODOLOGY FOR FILE IDENTIFICATION AND RETRIEVAL IN DIRECTORIES FOR CATEGORIZED FILES   | ACCAPADI, JOS MANUEL |
| <u>11424668</u> | Not Issued | 25 | 06/16/2006 | METHODOLOGY FOR DIRECTORY CATEGORIZATION FOR CATEGORIZED FILES   | ACCAPADI, JOS MANUEL |
| <u>11463100</u> | Not Issued | 30 | 08/08/2006 | System and Method for Dynamically Adjusting Read Ahead Values Based Upon Memory Usage  | ACCAPADI, JOS MANUEL |
| <u>11621637</u> | Not Issued | 25 | 01/10/2007 | METHOD FOR TRADING RESOURCES BETWEEN   | ACCAPADI, JOS MANUEL |

|          |               |    |            |   |                         |
|----------|---------------|----|------------|---|-------------------------|
|          |               |    |            | PARTITIONS OF A DATA<br>PROCESSING SYSTEM   |                         |
| 11692579 | Not<br>Issued | 19 | 01/01/0001 | METHOD AND APPARATUS<br>FOR SHARING BUFFERS | ACCAPADI, JOS<br>MANUEL |

Inventor Search Completed: No Records to Display.

**Search Another: Inventor**

|                                       |                                  |
|---------------------------------------|----------------------------------|
| <b>Last Name</b>                      | <b>First Name</b>                |
| <input type="text" value="Accapadi"/> | <input type="text" value="jos"/> |
| <input type="button" value="Search"/> |                                  |

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Day : Wednesday

Date: 3/28/2007

Time: 19:30:13

**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = DUNSHEA

First Name = ANDREW

| <b>Application#</b> | <b>Patent#</b> | <b>Status</b> | <b>Date Filed</b> | <b>Title</b>  | <b>Inventor Name</b> |
|---------------------|----------------|---------------|-------------------|---|----------------------|
| 09732144            | Not Issued     | 161           | 12/07/2000        | Multi-server system dynamic re-configuration  | DUNSHEA, ANDREW      |
| 09888468            | Not Issued     | 168           | 06/25/2001        | Method and apparatus for managing a cache   | DUNSHEA, ANDREW      |
| 10165082            | Not Issued     | 168           | 06/06/2002        | Effective garbage collection from a Web document distribution cache at a World Wide Web source site | DUNSHEA, ANDREW      |
| 10286532            | Not Issued     | 168           | 10/31/2002        | System and method for preferred memory affinity   | DUNSHEA, ANDREW      |
| 10422037            | Not Issued     | 71            | 04/23/2003        | System and method for thread prioritization during lock processing                                  | DUNSHEA, ANDREW      |
| 10614628            | 7080220        | 150           | 07/03/2003        | PAGE REPLACEMENT WITH A RE-REFERENCE INDICATOR  | DUNSHEA, ANDREW      |
| 10626192            | Not Issued     | 30            | 07/24/2003        | System and method for delayed priority boost  | DUNSHEA, ANDREW      |
| 10671057            | Not Issued     | 30            | 09/25/2003        | System and method for CPI load balancing in SMT processors  | DUNSHEA, ANDREW      |
| 10671132            | Not Issued     | 30            | 09/25/2003        | System and method for CPI scheduling on SMT processors  | DUNSHEA, ANDREW      |
| 10782668            | Not Issued     | 30            | 02/19/2004        | User defined preferred DNS reference  | DUNSHEA, ANDREW      |
| 10803659            | Not Issued     | 25            | 03/18/2004        | Method and data processing system for per-chip thread queuing in a multi-processor system           | DUNSHEA, ANDREW      |
| 10828455            | 7120753        | 150           | 04/20/2004        | SYSTEM AND METHOD FOR DYNAMICALLY ADJUSTING READ AHEAD VALUES BASED UPON MEMORY USAGE               | DUNSHEA, ANDREW      |

|                 |            |    |            |  |                 |
|-----------------|------------|----|------------|--|-----------------|
| <u>10834498</u> | Not Issued | 30 | 04/29/2004 | Scheduling threads in a multi-processor computer   | DUNSHEA, ANDREW |
| <u>10855732</u> | Not Issued | 30 | 05/27/2004 | Method, computer program product, and data processing system for improving transaction-oriented client-server application performance          | DUNSHEA, ANDREW |
| <u>10860409</u> | Not Issued | 71 | 06/03/2004 | Nodelay per port   | DUNSHEA, ANDREW |
| <u>10916976</u> | Not Issued | 25 | 08/12/2004 | Scheduling threads in a multiprocessor computer  | DUNSHEA, ANDREW |
| <u>10916984</u> | Not Issued | 30 | 08/12/2004 | System, application and method of reducing cache thrashing in a multi-processor with a shared cache on which a disruptive process is executing | DUNSHEA, ANDREW |
| <u>10916985</u> | Not Issued | 25 | 08/12/2004 | System, apparatus and method of reducing adverse performance impact due to migration of processes from one CPU to another                      | DUNSHEA, ANDREW |
| <u>10996307</u> | Not Issued | 61 | 11/22/2004 | Interrupt thresholding for SMT and multi processor systems   | DUNSHEA, ANDREW |
| <u>11006112</u> | Not Issued | 71 | 12/07/2004 | System, method and computer program product for application-level cache-mapping awareness and reallocation requests                            | DUNSHEA, ANDREW |
| <u>11006127</u> | Not Issued | 71 | 12/07/2004 | System, method and computer program product for application-level cache-mapping awareness and reallocation                                     | DUNSHEA, ANDREW |
| <u>11006371</u> | Not Issued | 41 | 12/07/2004 | Efficient memory update process for on-the-fly instruction translation for well behaved applications executing on a weakly-ordered processor   | DUNSHEA, ANDREW |
| <u>11011243</u> | Not Issued | 71 | 12/14/2004 | Memory pacing  | DUNSHEA, ANDREW |
| <u>11104044</u> | Not Issued | 30 | 04/12/2005 | Page allocation management for virtual memory  | DUNSHEA, ANDREW |
| <u>11146453</u> | Not Issued | 30 | 06/06/2005 | Administration of locks for critical sections of computer programs in a computer that supports a multiplicity of logical partitions            | DUNSHEA, ANDREW |
| <u>11197616</u> | Not        | 30 | 08/04/2005 | Method and apparatus for   | DUNSHEA,        |

|                                 |            |    |            |  |                 |
|---------------------------------|------------|----|------------|--|-----------------|
|                                 | Issued     |    |            | establishing a cache footprint for shared processor logical partitions                         | ANDREW          |
| <a href="#"><u>11201642</u></a> | Not Issued | 30 | 08/11/2005 | Method and apparatus for aging data in a cache   | DUNSHEA, ANDREW |
| <a href="#"><u>11201657</u></a> | Not Issued | 25 | 08/11/2005 | Method for forwarding network file system requests and responses between network segments      | DUNSHEA, ANDREW |
| <a href="#"><u>11227031</u></a> | Not Issued | 30 | 09/15/2005 | Facilitating presentation and monitoring of electronic mail messages with reply by constraints | DUNSHEA, ANDREW |
| <a href="#"><u>11259898</u></a> | Not Issued | 30 | 10/27/2005 | System and method for implementing a fast file synchronization in a data processing system     | DUNSHEA, ANDREW |
| <a href="#"><u>11301104</u></a> | Not Issued | 30 | 12/12/2005 | System and method for optimized preemption and reservation of software locks                   | DUNSHEA, ANDREW |
| <a href="#"><u>11301113</u></a> | Not Issued | 25 | 12/12/2005 | Sharing a kernel of an operating system among logical partitions                               | DUNSHEA, ANDREW |
| <a href="#"><u>11334676</u></a> | Not Issued | 30 | 01/18/2006 | Email application smart paste entry feature  | DUNSHEA, ANDREW |
| <a href="#"><u>11422656</u></a> | Not Issued | 25 | 06/07/2006 | SHARING KERNEL SERVICES AMONG KERNELS  | DUNSHEA, ANDREW |
| <a href="#"><u>11463100</u></a> | Not Issued | 30 | 08/08/2006 | System and Method for Dynamically Adjusting Read Ahead Values Based Upon Memory Usage          | DUNSHEA, ANDREW |
| <a href="#"><u>11621637</u></a> | Not Issued | 25 | 01/10/2007 | METHOD FOR TRADING RESOURCES BETWEEN PARTITIONS OF A DATA PROCESSING SYSTEM                    | DUNSHEA, ANDREW |

Inventor Search Completed: No Records to Display.

|                                 |                                       |                                     |
|---------------------------------|---------------------------------------|-------------------------------------|
| <b>Search Another: Inventor</b> | <b>Last Name</b>                      | <b>First Name</b>                   |
|                                 | <input type="text" value="Dunshea"/>  | <input type="text" value="andrew"/> |
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Day : Wednesday

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**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = MICHEL

First Name = DIRK

| Application#             | Patent#    | Status | Date Filed | Title  | Inventor Name |
|--------------------------|------------|--------|------------|--|---------------|
| <a href="#">10286532</a> | Not Issued | 168    | 10/31/2002 | System and method for preferred memory affinity  | MICHEL, DIRK  |
| <a href="#">10422037</a> | Not Issued | 71     | 04/23/2003 | System and method for thread prioritization during lock processing   | MICHEL, DIRK  |
| <a href="#">10614628</a> | 7080220    | 150    | 07/03/2003 | PAGE REPLACEMENT WITH A RE-REFERENCE INDICATOR   | MICHEL, DIRK  |
| <a href="#">10626192</a> | Not Issued | 30     | 07/24/2003 | System and method for delayed priority boost   | MICHEL, DIRK  |
| <a href="#">10671057</a> | Not Issued | 30     | 09/25/2003 | System and method for CPI load balancing in SMT processors   | MICHEL, DIRK  |
| <a href="#">10671132</a> | Not Issued | 30     | 09/25/2003 | System and method for CPI scheduling on SMT processors   | MICHEL, DIRK  |
| <a href="#">10782668</a> | Not Issued | 30     | 02/19/2004 | User defined preferred DNS reference   | MICHEL, DIRK  |
| <a href="#">10834498</a> | Not Issued | 30     | 04/29/2004 | Scheduling threads in a multi-processor computer   | MICHEL, DIRK  |
| <a href="#">10865702</a> | Not Issued | 71     | 06/10/2004 | Method and apparatus for dynamic hosting partition page assignment   | MICHEL, DIRK  |
| <a href="#">10916984</a> | Not Issued | 30     | 08/12/2004 | System, application and method of reducing cache thrashing in a multi-processor with a shared cache on which a disruptive process is executing | MICHEL, DIRK  |
| <a href="#">10916985</a> | Not Issued | 25     | 08/12/2004 | System, apparatus and method of reducing adverse performance impact due to migration of processes from one CPU to another                      | MICHEL, DIRK  |
| <a href="#">11226965</a> | Not Issued | 30     | 09/15/2005 | Method and apparatus for improved data transmission  | MICHEL, DIRK  |

|                 |            |     |            |   |                 |
|-----------------|------------|-----|------------|---|-----------------|
|                 |            |     |            | through a data connection   |                 |
| <u>11259898</u> | Not Issued | 30  | 10/27/2005 | System and method for implementing a fast file synchronization in a data processing system            | MICHEL, DIRK    |
| <u>11301104</u> | Not Issued | 30  | 12/12/2005 | System and method for optimized preemption and reservation of software locks                          | MICHEL, DIRK    |
| <u>11385926</u> | Not Issued | 30  | 03/21/2006 | Method to improve system DMA mapping while substantially reducing memory fragmentation                | MICHEL, DIRK    |
| <u>10803659</u> | Not Issued | 25  | 03/18/2004 | Method and data processing system for per-chip thread queuing in a multi-processor system             | MICHEL, DIRK M. |
| <u>09269074</u> | 6159606    | 150 | 01/14/2000 | FIRE-RESISTANT GLAZING PANEL  | MICHELS, DIRK   |
| <u>11575269</u> | Not Issued | 19  | 01/01/0001 | Method for documenting property or possession and transfer of property or possession of a merchandise | MICHELSEN, DIRK |

Inventor Search Completed: No Records to Display.

|                                 |                                       |                                   |
|---------------------------------|---------------------------------------|-----------------------------------|
| <b>Search Another: Inventor</b> | <b>Last Name</b>                      | <b>First Name</b>                 |
|                                 | <input type="text" value="michel"/>   | <input type="text" value="dirk"/> |
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**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = FLEET

First Name = JAMES

| <b>Application#</b> | <b>Patent#</b> | <b>Status</b> | <b>Date Filed</b> | <b>Title</b>   | <b>Inventor Name</b>     |
|---------------------|----------------|---------------|-------------------|--|--------------------------|
| <u>09517403</u>     | 6358620        | 150           | 03/02/2000        | Unsaturated polyester resin compositions   | FLEET, JAMES VAN         |
| <u>09170898</u>     | 6336170        | 150           | 10/13/1998        | METHOD AND SYSTEM IN A DISTRIBUTED SHARED-MEMORY DATA PROCESSING SYSTEM FOR DETERMINING UTILIZATION OF SHARED-MEMORY INCLUDED WITHIN NODES BY A DESIGNATED APPLICATION | FLEET, JAMES VAN         |
| <u>10138890</u>     | Not Issued     | 41            | 05/02/2002        | System and method for thread scheduling with weak preemption policy  | FLEET, JAMES W. VAN      |
| <u>09729894</u>     | 6910212        | 150           | 12/04/2000        | SYSTEM AND METHOD FOR IMPROVED COMPLEX STORAGE LOCKS   | FLEET, JAMES WILLIAM VAN |
| <u>10896272</u>     | Not Issued     | 71            | 07/20/2004        | Method, system and program for recording changes made to a database  | FLEET, JAMES WILLIAM VAN |
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